10/728, 5119.





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Stephen W. Bedell, et al.

Docket: 17107

Patent No.: 6,972,247 B2

Dated: April 20, 2006

Issued: December 06, 2005

For: METHOD OF FABRICATING STRAINED

SI SOI WAFERS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Certificate

APR 2 7 2006

Of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Sir:

It appears that errors have been introduced in the course of printing the Patent issued in the above application, and it is respectfully requested that the Commissioner issue a Certificate of Correction in the following respects:

<u>Column 9, Line 56, Claim 14:</u>
"3E16 cm⁻¹." should read – 3E16 cm⁻². --

Column 10, Line 3, Claim 19:

"process of a spike anneal." should read -- process or a spike anneal --

Column 10, Line 16, Claim 23:

"process of a spike anneal" should read -- process or a spike anneal --

<u>Column 12, Line 32, Claim 53:</u>
"from about to about 500 nm" should read – from about 10 to about 500 nm --

Respectfully submitted,

Léslie S. Szivos Reg. No. 39,394

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UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO

: 6,972,247 62

APPLICATION NO: 10/728,519

ISSUE DATE

: December 06, 2005

INVENTOR(S)

: Stephen W. Bedell, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, Line 56, Claim 14:

"3E16 cm⁻¹." should read - 3E16 cm⁻². --

Column 10, Line 3, Claim 19:

"process of a spike anneal." should read -- process or a spike anneal --

Column 10, Line 16, Claim 23:

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"from about to about 500 nm" should read -- from about 10 to about 500 nm --

MAILING ADDRESS OF SENDER:

PATENT No. 6,972,247 \(\beta 2 \)

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